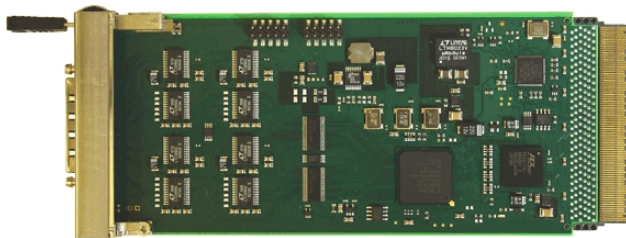


# TAMC863 4 Channel High Speed Synch/Asynch Serial Interface

### Application Information

The TAMC863 is a standard single Mid-Size/Full-Size AMC.1 compliant module with four high speed serial data communication channels.

The serial communication controller is implemented in FPGA logic, along with a bus master capable PCI interface, guaranteeing long term availability with the option to implement additional functions in the future. The FPGA is connected to the PCI Express interface via a PCI Express to PCI bridge.



Each channel has a receive and a transmit FIFO of 512 long words (32 bit) per channel for high data throughput.

Data transfer on the PCI bus is handled via TAMC863 initiated DMA cycles with minimum host/CPU intervention.

Several serial communication protocols are supported by each channel, such as asynchronous, isochronous, synchronous and HDLC mode.

A 14.7456 MHz oscillator provides standard asynchronous baud rates. An additional 24 MHz oscillator is provided for other baud rates. A 10 MHz oscillator is used for the synchronous baud rate of 10 Mbit/s.

Each channel also provides various interrupt sources, generated on INTA. The interrupt sources can be enabled or disabled individually.

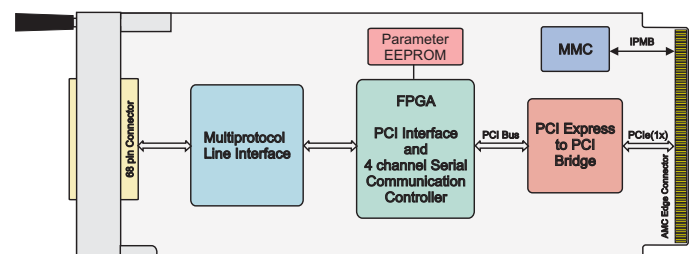
Multiprotocol transceivers are used for the line interface. The physical interface is selectable by software, individually for each channel as EIA-232, EIA-422, EIA-449, EIA-530, EIA-530A, V.35, V.36 or X.21.

The following signals are provided by the TAMC863 for each channel at the front I/O connector: Receive Data (RxD +/-), Transmit Data (TxD +/-), Receive Clock (RxC +/-), Transmit Clock (TxC +/-), Ready-To-Send (RTS +/-), Clear-To-Send (CTS +/-), Carrier-Detect (CD +/-) and GND. Additionally, serial channel 3 provides Data-Set-Ready (DSR3 +/-) and Data-Terminal-Ready (DTR3 +/-).

For First-Time-Buyers the engineering documentation TAMC863-ED is recommended. The engineering documentation includes TAMC863-DOC, schematics and data sheets of TAMC863 devices.

### Technical Information

- Form Factor: PCIMG AMC.1 Module
  - Board size: 180.6mm x 73.5mm
  - Single Mid Size / Full-Size
- PCIe single lane (x1) port (AMC.1 Type 1 compliant)
- IPMI Support
- Front Panel LEDs:
  - Blue Hot-Swap LED
  - Red Failure LED (LED1)
  - Green Power Good LED (LED 2)
- Four high speed synchronous/asynchronous serial interfaces
- Support of RxD, TxD, RxC, TxC, RTS, CTS, CD and GND on front connector, DTR3 and DSR3 on channel 3 only
- Physical interface (individually programmable per channel):  
EIA-232, EIA-422, EIA-449, EIA-530, EIA-530A, V.35, V.36 and X.21
- Maximum data rate: 10 Mbit/s (synchronous), 2 Mbit/s (asynchronous), internal or external provided clock
- EIA-232: up to 115.2 kbit/s
- Temperature range: -40°C to +85°C



### Order Information

#### RoHS Compliant

<b>TAMC863-10R</b>	4 Channel High Speed Synch/Asynch Serial Interface, Mid-Size front panel
<b>TAMC863-11R</b>	4 Channel High Speed Synch/Asynch Serial Interface, Full-Size front panel

#### None RoHS Compliant

TAMC863-10	None RoHS compliant version of TAMC863-10R
TAMC863-11	None RoHS compliant version of TAMC863-11R

#### Documentation

<b>TAMC863-DOC</b>	User Manual
<b>TAMC863-ED</b>	Engineering Documentation (TAMC863-DOC, Schematics, Data Sheets)

#### Software

<b>TDRV009-SW-25</b>	Integrity Software Support
<b>TDRV009-SW-42</b>	VxWorks Software Support (Legacy and VxBus-Enabled Software Support)
<b>TDRV009-SW-65</b>	Windows XP/XPE2000 Software Support
<b>TDRV009-SW-72</b>	LynxOS Software Support
<b>TDRV009-SW-82</b>	LiNIX Software Support
<b>TDRV009-SW-95</b>	QNX 6 Software Support

For other operating systems please contact TEWS.

#### Related Products

<b>TA307</b>	Cable Kit for Modules with VHD68 connector
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