The Embedded I/O Company

TIP675 48 TTL I/O Lines with Interrupts

Application Information

The TIP675 is an IndustryPack® compatible module providing 48 digital TTL tri-state I/O lines with pull up resistors. Each of the 48 I/O lines is ESD protected and protected against overvoltage.

The line inputs are always enabled, allows determining the state of the I/O line at any time. This can be used as read back function for lines configured as outputs.

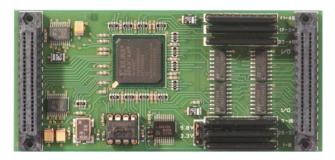
All 48 input lines can generate an interrupt. Each input interrupt can individually be enabled and cleared. Interrupts for negative and positive transitions could be used together with separate interrupt pending registers.

All interrupt inputs have an electronic debounce circuit to prevent short spikes on input lines to cause an IP interrupt.

Optional the I/O lines can be configured for simultaneous update internally and across several IP's by an external clock source. The polarity of the external clock source is programmable.

6 resistor networks mounted in sockets are used to pull the tri-state I/O lines to a logic high value. The resistor networks can be removed or changed in value.

After power-on or reset all I/O lines are configured as input and all pending interrupts are cleared.

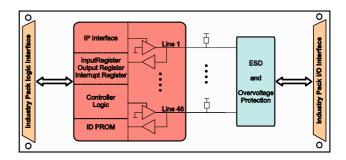


For First Time Users the Engineering Documentation TIP675-ED is recommended. The Engineering Documentation includes TIP675-DOC, schematics and data sheets.

Software Support (TIP675-SW-xx) is available for different operating systems.

Technical Information

- Interface according to IndustryPack specification \mathbf{O}
- Identification-PROM supports auto-configuration
- 0 Single Size IndustryPack
- 48 digital TTL tri-state I/O lines with pull up resistors \mathbf{O}
- I/O lines individually programmable as input or output
- 0 Full interrupt capability on inputs
- O Electronic debounce circuit for interrupt
- O ESD and overvoltage protection for each I/O line
- O Resistor networks mounted in sockets as I/O line pull
- Programmable simultaneous update feature by \mathbf{O} external clock source (TTL level)
- Operating temperature range -40°C to +85°C



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Order Information

RoHS Compliant
TIP675-10R

48 digital TTL tri-state I/O Lines with Interrupts

TIP675-SW-25
TIP675-SW-42

Integrity Software Support VxWorks Software Support

(Legacy and VxBus-Enabled Software Support)

Software

None RoHS Compliant Software Support)

TIP675-10 None RoHS compliant version of TIP675-SW-65 Windows XP/XPE/2000 Software

TIP675-10R Support

DocumentationTIP675-SW-72LynxOS Software SupportTIP675-DOCUser ManualTIP675-SW-95LiNUX Software SupportQNX 6 Software Support

TIP675-ED Engineering Documentation, includes
TIP675-DOC For other operating systems please contact TEWS.

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09/2009

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